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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO	
09/994,620	11/28/2001	Takamasa Hayashi	216563US2	9269	
22850	7590 09/23/2005		EXAMINER		
OBLON, SPIVAK, MCCLELLAND, MAIER & NEUSTADT, P.C.			EBRAHIMI DEHKORDY, SAEID		
1940 DUKE S ALEXANDRI	A, VA 22314	ART UNIT	PAPER NUMBER		
			2626		

DATE MAILED: 09/23/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

		Applicatio	n No.	Applicant(s)				
Office Action Summary		09/994,620		HAYASHI ET AL.				
		Examiner		Art Unit	· · · · · · · · · · · · · · · · · · ·			
		Saeid Ebra	himi-dehKordy	2626				
	The MAILING DATE of this communica		· · · · · · · · · · · · · · · · · · ·	e correspondence ad	dress			
Period for Reply								
THE - Extra afte - If th - If No - Fail	HORTENED STATUTORY PERIOD FOR MAILING DATE OF THIS COMMUNICAl ensions of time may be available under the provisions of 3 or SIX (6) MONTHS from the mailing date of this communical eperiod for reply specified above is less than thirty (30) of period for reply is specified above, the maximum statutor to reply within the set or extended period for reply will, reply received by the Office later than three months after need patent term adjustment. See 37 CFR 1.704(b).	ATION. IT CFR 1.136(a). In no ever cation. ays, a reply within the statur ory period will apply and will by statute, cause the appli	nt, however, may a reply be tory minimum of thirty (30) I expire SIX (6) MONTHS fi cation to become ABANDC	e timely filed days will be considered timely rom the mailing date of this co NED (35 U.S.C. § 133).	<i>y.</i> ommunication.			
Status								
1)	Responsive to communication(s) filed of	on						
2a)□	•							
3)								
	closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11, 453 O.G. 213.							
Disposit	tion of Claims							
4)⊠	Claim(s) <u>1-6</u> is/are pending in the application.							
	4a) Of the above claim(s) is/are withdrawn from consideration.							
5)[Claim(s) is/are allowed.							
6)🖾	Claim(s) 1.5 and 6 is/are rejected.							
7)⊠	Claim(s) 2-4 is/are objected to.							
8)[Claim(s) are subject to restriction and/or election requirement.							
Applicat	tion Papers		-					
9) The specification is objected to by the Examiner.								
10)⊠ The drawing(s) filed on <u>28 November 2001</u> is/are: a)⊠ accepted or b)□ objected to by the Examiner.								
	Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).							
	Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).							
11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.								
Priority	under 35 U.S.C. § 119							
	Acknowledgment is made of a claim for ⊠ All b) Some * c) None of: 1. Certified copies of the priority do		-	(a)-(d) or (f).				
	2. Certified copies of the priority do			ation No				
	3. Copies of the certified copies of t				Stage			
	application from the International	•						
* See the attached detailed Office action for a list of the certified copies not received.								
Attachmer	nt(s)		•					
1) Notice of References Cited (PTO-892) 4) Interview Summary (PTO-413)								
	ce of Draftsperson's Patent Drawing Review (PTO- mation Disclosure Statement(s) (PTO-1449 or PTO		Paper No(s)/Mail	l Date al Patent Application (PTC)-152)			
	rnauon bisclosure statement(s) (P10-1449 of P10 er No(s)/Mail Date <u>8/2/04,7/9/04,3/18</u> .		,					

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DETAILED ACTION

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- 2. Claims 1 and 5-6 are rejected under 35 U.S.C. 102(b) as being anticipated by Applegate et al (U.S. patent 5,995,774)

Regarding claim 1 Applegate et al disclose: An image forming apparatus (Fig.1 item 10, column 8 lines 50-60) comprising: an apparatus body (note Fig.1 item 10) image forming means at least partly implemented by a replaceable part which is removably mounted to said apparatus body (note Fig.1 item 100, column 9 lines 63-67 and column 10 lines 1-4 also note column 5 lines 7-14) sensing means (note Fig.5 item 172 the gas gauge sensor, column 11 lines 30-37) for sensing a condition of use of the replaceable part that varies in accordance with use of said apparatus body (note Fig.5 column 11 lines 30-53) first writable and readable non-volatile storing means built in said apparatus body (note Fig.5 item 152 and 162 the NVRAM, non volatile memories, column 11 lines 20-29) second writable and readable non-volatile storing means built in the replaceable part (note column 1 lines 6-16 and specifically lines 10-16, also note abstract lines 1-4, also note column 10 lines 29-38 and also column 30 lines 1-8) accessing means for accessing said first storing means and said second storing means via a shared data bus (note Fig.5 item 176 where the print engine which is connected to devices 162 and 152

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"non-volatile memories communicates with the non-volatile memory 144 of the cartridge, column 11 lines 30-37) and control means for sensing at a time of image formation (Fig.14 and 5, item 172, column 26 lines 14-31) a variation of the condition of use of the replaceable part via said sensing means (note Fig.12,13 and 14, column 23-26 where the complete consumption of the toner used and the degree of variation is fully taught by Applegate) obtaining information representative of a condition after use from a sensed variation (note again Figs.12,13 and 14 where the condition after use and variation of it is shown in terms of consumption and assessed and actual use, columns 23-26) writing among said information, information relating to operation specifications of said apparatus body in said second storing means as well as in said first storing means (note column 10 lines 29-37 and also note Fig.8, column 20 lines 1-17).

Regarding claim 5 Applegate et al disclose: In an IC (Integrated Circuit) chip (note column 9 lines 45-54 and specifically lines 56-57 where the integrated circuit is embedded in the RAM of the printer 10 which communicates with the engine controller) connected to a CPU (Central Processing Unit) (note column 11 lines 20-29 where the integrated circuit is connected which is part of the NVRAM is communicating with the engine controller160) which is built in an apparatus body of an image forming apparatus (note Fig.5 items 162 the NVRAM with integrated circuit embedded and controller 160 are housed in the body of the apparatus10) when mounted to said apparatus body and including writable and readable nonvolatile storing means accessible under a control of said CPU (note column 9 lines 46-61 where the integrated circuit is embedded to the

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NVRAM memory which interact with the controller 36 of Fig.1 and also note Fig.5 column 11 lines 20-37) an access to said nonvolatile storing means is made via a data bus shared by said nonvolatile storing means and writable and readable nonvolatile storing means built in said apparatus body (note Fig.5 and items 152,162 and the device 10 the printer apparatus which houses these items and also note column 9 lines 36-44 where the buses are provided as electronic pathways to make communication between the elements of printer 10) and among information representative of a condition of operation of said apparatus body that varies in accordance with an operation of said apparatus body information relating to operation specifications of said apparatus body is written to said storing means of said IC chip when said IC chip is mounted to said apparatus body (note column 9 lines 46-55 and column 11 lines 20-37 and also column).

Regarding claim 6 Applegate et al disclose: In a replaceable part for an image forming apparatus including image forming means that is at least partly removable from an apparatus body of said image forming apparatus (note Fig.1 item 100, column 9 lines 63-67 and column 10 lines 1-4 also note column 5 lines 7-14) said replaceable part includes an IC chip (note Fig.6 item 144 which has embedded integrated circuit, column 12 lines 52-67 and also column 11 lines 20-37 where integrated circuit is embedded in the NVRAM of the memory 144 of the cartridge) connected to a CPU (note Fig.5 item 176 which connects the engine controller of the printer 10 to the NVRAM and embedded integrated circuit of the cartridge, column 10 lines 63-67 and column 11 lines 1-37) which is built in said apparatus body (note Fig.5 item 10 the printer and item 160

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the controller or Cup of the printer 10) when mounted to said apparatus body and including writable and readable nonvolatile storing means accessible under a control of said CPU (note Fig.5 items 152 and 160 which are respectively NVRAM and the controller communicating through the buses, column 9 lines 36-44) an access to said nonvolatile storing means is made via a data bus shared by said nonvolatile storing means and writable and readable nonvolatile storing means built in said apparatus body (note Fig.5 and items 152,162 and the device 10 the printer apparatus which houses these items and also note column 9 lines 36-44 where the buses are provided as electronic pathways to make communication between the elements of printer 10) and among information representative of a condition of operation of said apparatus body that varies in accordance with an operation of said apparatus body, information relating to operation specifications of said apparatus body is written to said storing means of said IC chip when said IC chip is mounted to said apparatus body (note column 9 lines 46-55 and column 11 lines 20-37 and also column).

Allowable Subject Matter

3. Claims 2-4 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims, the operation specifications in accordance with the information stored in said first storing means and representative of a condition of the last use causes said apparatus body to start operating under image forming conditions based on said operation specifications, and again determines, if the condition of the last use stored in said first storing means and the condition of the last use stored in said second

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storing means do not compare equal, whether or not to again set image forming conditions in accordance with new operation specifications based on said condition stored in said second storing means would be novel.

Contact Information

➤ Any inquiry concerning this communication or earlier communications from the examiner should be directed to *Saeid Ebrahimi-Dehkordy* whose telephone number is (571) 272-7462.

The examiner can normally be reached on Monday through Friday from 8:00 a.m. to 5:30 p.m. If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Kimberly Williams, can be reached at (571) 272-7471.

Any response to this action should be mailed to:

Assistant Commissioner for Patents Washington, D.C. 20231

Or faxed to:

(571) 273-8300, (for *formal* communications; please mark "EXPEDITED PROCEDURE")

Or:

(703) 306-5406 (for *informal* or *draft* communications, please label "PROPOSED" or "DRAFT")

Hand delivered responses should be brought to Knox building on 501 Dulany Street, Alexandria, VA.

Any inquiry of a general nature or relating to the status of this application should be directed to the Group Receptionist whose telephone number is (703) 305-4750.

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Saeid Ebrahimi-Dehkordy Patent Examiner

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August 17, 2005

KIMBERLY WILLIAMS

SUPERVISORY PATENT EXAMINER